

Features

- 8, 16, 32, 64 points I/O module
- Operation monitoring by LED display
- Easy maintenance: Terminal block type, one-touch installation of module



Input module specifications

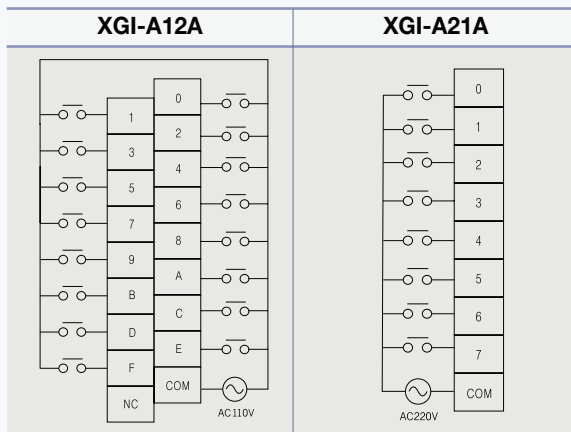
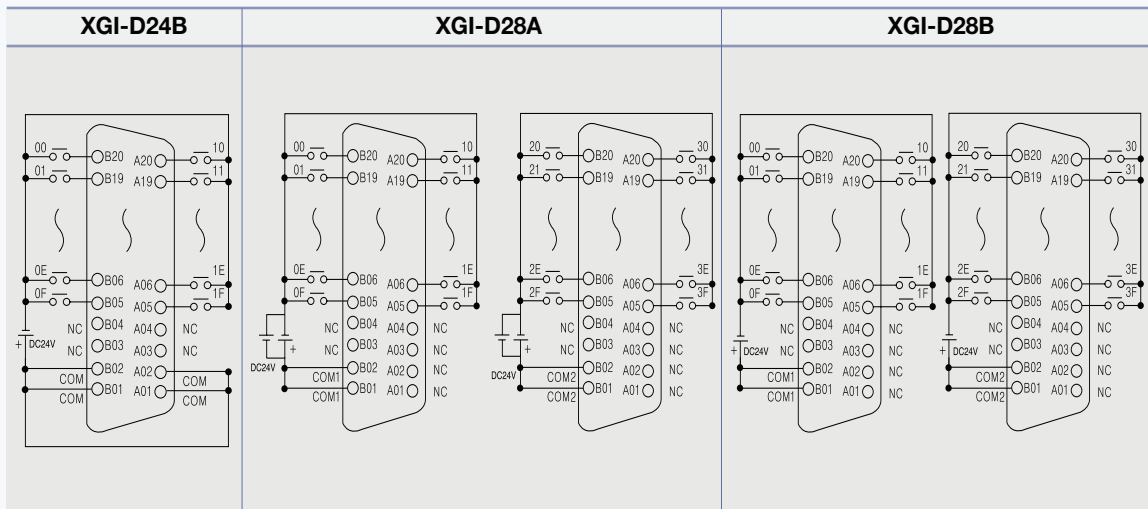
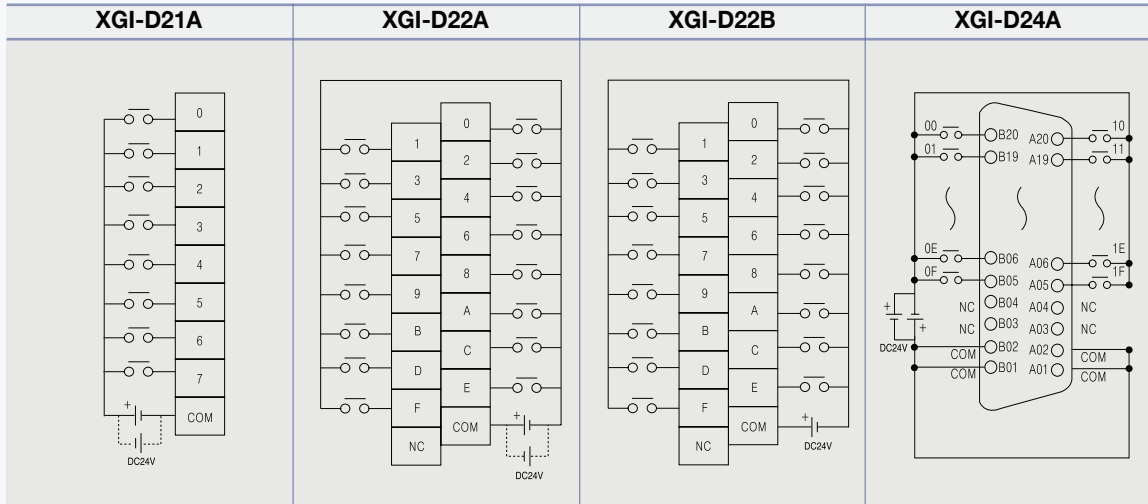
Input type		DC input						AC input		
Type		XGI-D21A	XGI-D22A	XGI-D22B	XGI-D24A	XGI-D24B	XGI-D28A	XGI-D28B	XGI-A12A	XGI-A21A
Input point		8	16		32		64		16	8
Rated input voltage		DC24V						AC100~120V		Free voltage
Rated input current		4mA						8mA		17mA
ON voltage/current		19V or more / 3mA or less						AC80V or more / 5mA or less		AC130V or more / 10mA or less
OFF voltage/current		DC11V or more / 1.7mA or less						AC30V or more / 1mA or less		AC60V or more / 2mA or less
Response	Off→On	1ms/5ms/10ms/20ms/70ms (set by CPU parameter) Initial value: 3ms						15mA or less		
	On→Off	1ms/5ms/10ms/20ms/70ms (set by CPU parameter) Initial value: 3ms						25mA or less		
Common (COM)		8 points/COM	16 points/COM		32 points/COM		16 points/COM		8 points/COM	
Insulation method		Photocoupler								
Current consumption (mA)		20	30		50		60		30	20
Weight (Kg)		0.1	0.12		0.1		0.15		0.13	0.13

Output module specifications

Input type		Relay			Transistor				Triac		
Type		XGQ-RY1A	XGQ-RY2A	XGQ-RY2B	XGQ-TR2A	XGQ-TR2B	XGQ-TR4A	XGQ-TR4B	XGQ-TR8A	XGQ-TR8B	XGQ-SS2A
Output point		8	16		16		32		64		16
Rated load voltage		DC12/24V, AC110/220V				DC12/24V				AC110/220V	
Rated output current	1 point	2A			0.5A		0.1A				0.6A
	Common	5A			4A		2A				4A
Response time	Off→On	10ms or less			1ms or less						1ms or less
	On→Off	12ms or less			1ms or less						0.5cycle +1ms or less
Common (COM)		1 point/COM	16 points/COM		32 points/COM				16 points/COM		
Insulation method		Relay			Photocoupler						
Current consumption (mA)		260	500		70		130		230		300
Weight (Kg)		0.13	0.17	0.19	0.11		0.1		0.15		0.2
Surge killer		-		Varistor		Zener diode				Varistor	
External power supply		-			DC				-		

Note) B1, B2 of 32, 62 points terminal (connector) are shorted inside of the product.

Wiring diagram for input modules



Wiring diagram for output modules

CPU

